

s_thyristor_1 (subcircuit)

Attributes

```
inputs: g_in
outputs:
e_left_nodes:
e_right_nodes:
e_top_nodes: anode
e_bottom_nodes: cathode
b_left_nodes:
b_right_nodes:
b_top_nodes:
b_bottom_nodes:
parameters:
  cap: 10p
  r_off: 1M
  r_on: 1m
  v_on: 0
  x_high: 1
```

Description

s_thyristor_1 is a thyristor connected between anode and cathode in parallel with a capacitor. The thyristor parameters are specified by r_on, r_off, v_on, x_high (see the documentation for thyristor.ebe). The g_in port serves as the gate of the thyristor. The parallel capacitance is given by the parameter cap.

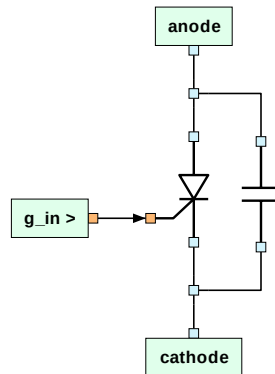


Figure 1: Schematic diagram of s_thyristor_1.