

s_gate_pulses_3ph_1 (subcircuit)

Attributes

```
inputs: a b c ref
outputs: g1 g2 g3 g4 g5 g6
e_left_nodes:
e_right_nodes:
e_top_nodes:
e_bottom_nodes:
parameters:
  delt_min: 0.1u
  delt_nrml: 10u
  t_delay: 0.1m
  y_high: 1
```

Description

s_gate_pulses_3ph_1 is a three-phase extension of s_gate_pulses_1ph_1. It compares inputs a, b, c with ref, and produces output pulses g1 to g6, using three s_gate_pulses_1ph_1 subcircuits. Further details can be found in the documentation for s_gate_pulses_1ph_1.