s_vsi_3ph_2a (subcircuit)

Attributes

```
inputs: g1 g2 g3 g4 g5 g6
outputs:
e_left_nodes:
e_right_nodes: a b c
e_top_nodes: p
e_bottom_nodes: m
b_left_nodes:
b_right_nodes:
b_top_nodes:
b_bottom_nodes:
parameters:
  c_s: 10p
  r_off: 10M
  r_on: 1m
  r_s: 0.01
  x_high: 1
```

Description

s_vsi_3ph_2a is the 3-phase voltage source inverter circuit shown in the figure with the gate signals being externally supplied. Each of the switch-diode blocks has a series *RC* snubber in parallel (see the documentation for s_switch_diode_1.ebe). The parameters r_on, r_off, x_high are related to the switch-diode pair, and r_s, c_s to the series snubber branch.
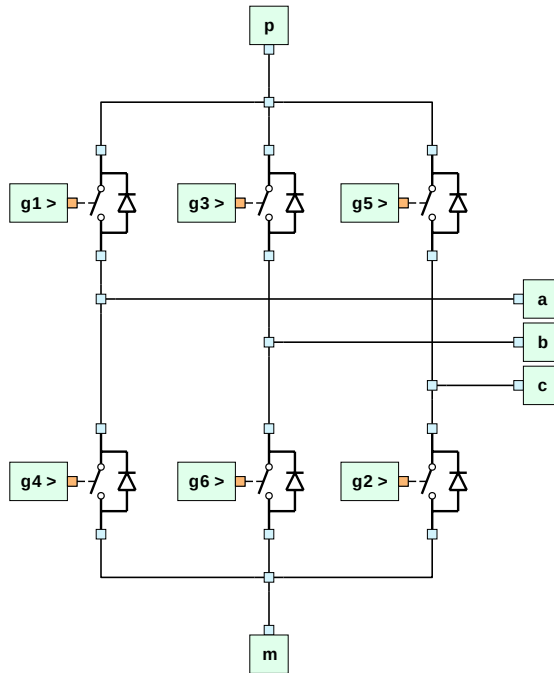

Figure 1: Schematic diagram of s_vsi_3ph_2a.