## s\_rl\_star\_bus\_1 (subcircuit)

## **Attributes**

inputs:
outputs:
e\_left\_nodes:
e\_right\_nodes: n
e\_top\_nodes:
e\_bottom\_nodes:
b\_left\_nodes: abc
b\_right\_nodes:
b\_top\_nodes:
b\_bottom\_nodes:
parameters:
L: 1u
R: 10

## **Description**

 $s_rl_star_bus_1$  is a star-connected RL load, with the a, b, c ports brought out as a single bus port (see figure).



Figure 1: Schematic diagram of s\_rl\_star\_bus\_1.