

## **srff\_nand.xbe**

### **Attributes**

```
xbe name=srff_nand evaluate=yes save_history=yes allow_ssw=no
#
# SR flip-flop (cross-coupled NAND gates)
# Assume zero gate delay.
#
Jacobian: variable
input_vars: s r
output_vars: q qbar
aux_vars:
iparms:
sparms:
rparms:
+ x_high=1
+ x_cross=0
+ q_prev=0
stparms: q_st=0
igparms:
outparms: s r q
```

### **Description**

**srff\_nand.xbe** is a SR flip-flop with inputs **s**, **r**, and outputs **q**, **qbar**. It mimics the behaviour of an SR latch with cross-coupled NAND gates. The start-up parameter **q\_st** gives the **q** value in start-up simulation. **s**, **r**, **q** are made available as output variables. The parameter **x\_high** specifies the high level in both input and output waveforms.